



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Weber  
Serial No. : 09/898,909  
Filed : July 3, 2001  
Title : METALLIZATION ARRANGEMENT FOR SEMICONDUCTOR STRUCTURE  
AND CORRESPONDING FABRICATION METHOD

Art Unit : 2814  
Examiner : P. Cao

Commissioner for Patents  
Washington, D.C. 20231

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TECHNOLOGY CENTER 2800  
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#8116-02  
9-Artin

RESPONSE TO RESTRICTION REQUIREMENT

In response to the office action mailed October 2, 2002, Applicant elects the invention of Group II, drawn to the embodiment of a process for making a semiconductor device. The election is made without traverse.

Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: October 22, 2002

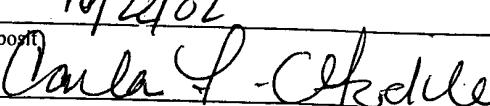
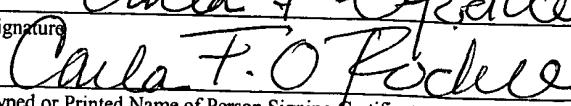
  
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I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

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